



CRYSTALS–Kyber

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<https://pq-crystals.org/kyber>

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Reminder: the big picture

Kyber.CPAPKE: LPR encryption or “Noisy ElGamal”

$$\mathbf{s}, \mathbf{e} \leftarrow \chi$$

$$sk = \mathbf{s}, pk = \mathbf{t} = \mathbf{A}\mathbf{s} + \mathbf{e}$$

$$\mathbf{r}, \mathbf{e}_1, \mathbf{e}_2 \leftarrow \chi$$

$$\mathbf{u} \leftarrow \mathbf{A}^T \mathbf{r} + \mathbf{e}_1$$

$$v \leftarrow \mathbf{t}^T \mathbf{r} + \mathbf{e}_2 + \text{Enc}(m)$$

$$c = (\mathbf{u}, v)$$

$$m = \text{Dec}(v - \mathbf{s}^T \mathbf{u})$$

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Kyber.CCAKEM: CCA-secure KEM via tweaked FO transform

- Use implicit rejection
- Hash public key into seed and shared key
- Hash ciphertext into shared key
- Use Keccak-based functions for all hashes and XOF

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- Compress ciphertexts (round off least-significant bits)
- Compress public keys

“We note that a potential issue is that the security proof does not directly apply to Kyber itself, but rather to a modified version of the scheme which does not compress the public key.”

—NIST IR 8240

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Kyber sizes, round 1 vs. round 2

Kyber512 ($k = 2$, level 1)			
round 1, sizes in bytes		round 2, sizes in bytes	
pk:	736	pk:	800
ct:	800	ct:	736

Kyber768 ($k = 3$, level 3)			
round 1, sizes in bytes		round 2, sizes in bytes	
pk:	1088	pk:	1184
ct:	1152	ct:	1088

Kyber1024 ($k = 4$, level 5)			
round 1, sizes in bytes		round 2, sizes in bytes	
pk:	1440	pk:	1568
ct:	1504	ct:	1568

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 - Faster noise sampling
6. Represent public key in NTT domain
 - Save several NTT computations

Kyber is fast

Kyber512 ($k = 2$, level 1)

Sizes (in Bytes)

sk: 1632
pk: 800
ct: 736

Haswell Cycles (AVX2)

gen: 29100
enc: 46196
dec: 39410

Kyber768 ($k = 3$, level 3)

Sizes (in Bytes)

sk: 2400
pk: 1184
ct: 1088

Haswell Cycles (AVX2)

gen: 57340
enc: 78692
dec: 68620

Kyber1024 ($k = 4$, level 5)

Sizes (in Bytes)

sk: 3168
pk: 1568
ct: 1568

Haswell Cycles (AVX2)

gen: 81244
enc: 109584
dec: 97280

Kyber is fast and small

Kyber512 ($k = 2$, level 1)

Stack usage (in Bytes)

gen: 2952

enc: 2552

dec: 2560

Cortex-M4 Cycles

gen: 513992

enc: 652470

dec: 620946

Kyber768 ($k = 3$, level 3)

Stack usage (in Bytes)

gen: 3848

enc: 3128

dec: 3072

Cortex-M4 Cycles

gen: 976205

enc: 1146021

dec: 1094314

Kyber1024 ($k = 4$, level 5)

Stack usage (in Bytes)

gen: 4360

enc: 3584

dec: 3592

Cortex-M4 Cycles

gen: 1574351

enc: 1779192

dec: 1708692

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 - Risk to make wrong decision about *lattice* design from “symmetrically tainted” benchmarks
- Maybe just a small problem, because lattice-based KEMs are all fast enough
- Better to decide based on
 - size/bandwidth
 - RAM/ROM footprint and gate count in HW
 - simplicity
 - how conservative designs are
 - cost of SCA protection

Kyber-90s performance (Haswell cycles)

Kyber512 ($k = 2$, level 1)

Kyber cycles

gen: 29100

enc: 46196

dec: 39410

Kyber-90s cycles

gen: 15792

enc: 26612

dec: 22248

Kyber768 ($k = 3$, level 3)

Kyber cycles

gen: 57340

enc: 78692

dec: 68620

Kyber-90s cycles

gen: 25632

enc: 39976

dec: 33744

Kyber1024 ($k = 4$, level 5)

Kyber cycles

gen: 81244

enc: 109584

dec: 97280

Kyber-90s cycles

gen: 38164

enc: 57280

dec: 50360

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